LISTING OF CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the

application:

1.   (Currently Amended) An electronic device comprising:

       a housing to enable the device to be docked into a notebook computer

              having a memory to store [an] a first operating system;

       an interface disposed on a surface of the housing to enable communication

              between the device and the notebook computer when the device is

              docked;

       a memory to store [an] a second operating system, the second operating

              system is smaller in size and provides less functionality than the first

              operating system ; and

       a processor to operate as a system processor of the notebook computer

              when the device is docked and to operate as a system processor of the

              device when the device is undocked.

2.   (Original) The electronic device of claim 1, further comprising an input

       controller to receive input data into the device when the device is undocked.

3.   (Original) The electronic device of claim 2, further comprising core memory

       to store the input data when the device is undocked.

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4. (Original) The electronic device of claim 3, further comprising an output controller to provide output data from the device when the device is undocked.

5. (Original) The electronic device of claim 4, further comprising a visual display disposed on a surface of the housing, the visual display being coupled to the input controller to provide the input data via pen-based entries on the display and being coupled to the output controller to provide the output data via the display.

6. (Canceled)

7. (Original) The electronic device of claim 1, further comprising a battery to provide power to the processor when the electronic device is undocked.

8. (Original) The electronic device of claim 7, wherein the interface is coupled to the battery to charge the battery when the electronic device is docked.

9. (Original) The electronic device of claim 8, wherein the notebook computer is to provide power to the processor when the electronic device is docked.

10.   (Original) The electronic device of claim 9, wherein the processor is to operate
at a higher frequency and at a higher voltage when the device is docked than
when the device is undocked.

11.   (Original) The electronic device of claim 1, wherein the processor is to operate
at a higher frequency and at a higher voltage when the device is docked than
when the device is undocked.

12.   (Currently Amended) A base computer comprising:

a docking port to receive a hand-held core computer having a processor to

operate as a system processor of the base computer when the device is

docked and to operate as a system processor of the core computer

when the device is undocked, the hand-held core including a memory

to store [an] a first operating system;

an interface in the docking port to enable communication between the

core computer and the base computer when the core computer is

docked; and

a memory to store [an] a second operating system, the second operating

system is larger in size and has greater functionality than the first

operating system.

13.   (Canceled)

14. (Original) The base computer of claim 12, wherein the interface is to couple a power supply of the base computer to a battery in the core computer to charge the battery and to provide power to the processor when the core computer is docked.

15. (Original) The base computer of claim 14, wherein the processor is to operate at a higher frequency and at a higher voltage when the processor operates as a system processor of the base computer than when the processor operates as a system processor of the core computer.

16. (Original) The base computer of claim 12, wherein the processor is to operate in one of a high power mode and a low power mode according to user preference.

17. (Currently Amended) A method of operating a computer system comprising:

    operating a processor as a system processor of a notebook computer when a core computer is docked in a docking port of the notebook computer, the notebook computer including a memory to store an operating system; and

operating the processor as a system processor of the core computer when the core computer is undocked, the core computer including a memory to store an operating system.

18.   (Original) The method of claim 17, further comprising synchronizing memory of the notebook computer with memory of the core computer when the core computer is docked.

19.   (Original) The method of claim 17, further comprising charging a battery in the core computer when the core computer is docked.

20.   (Original) The method of claim 17, wherein operating the processor as a system processor of the notebook computer includes operating the processor at a higher frequency and voltage than when operating the processor as a system processor of the core computer.

21.   (Canceled)